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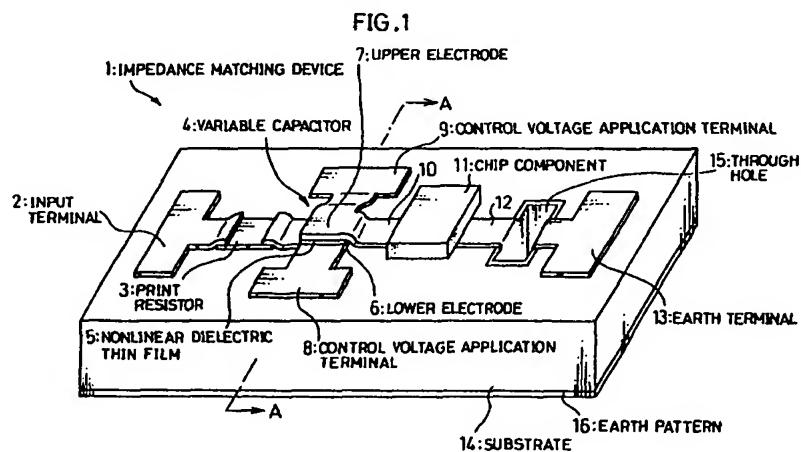
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## (54) Impedance matching device

(57) An impedance matching device includes a variable capacitor constituted by a non-linear dielectric thin film and an upper electrode disposed on a lower electrode formed on a substrate. The non-linear dielectric thin film is formed by a deposition, screen-printing, electroplating, or other technique, and changes its relative dielectric constant according to applied voltage. Therefore, the capacity of the variable capacitor is controlled

and the impedance is matched, by simply controlling the applied voltage across the non-linear dielectric thin film. Consequently, the arrangement makes it possible to cut down the impedance matching device in size and cost, compared to an arrangement incorporating a capacitor and a coil for adjustment, effects a much simpler matching operation, and facilitates a manufacturing process.



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**Description****FIELD OF THE INVENTION**

[0001] The present invention relates to an impedance matching device suitably used for an impedance matching antenna, directional antenna, etc. incorporated in a high frequency wireless device, satellite broadcast receiver, wireless LAN system, and other radio high frequency devices handling signals in a frequency range of about 400MHz to 20GHz (from the UHF to SHF band).

**BACKGROUND OF THE INVENTION**

[0002] Many efforts have recently been made to minimise the sizes and prices, and at the same time maximise the function, of high frequency devices including a high frequency wireless device, satellite broadcast receiver, and a wireless LAN system. Accordingly, demands are high for smaller, thinner, and cheaper electronics components for high frequency circuits incorporated in those devices. In addition, a protrusion, such as an antenna, on a portable device is not a desirable feature for aesthetic and portability reasons.

[0003] In a move to respond to these demands, dielectric ceramic small block antennas have been introduced into market.

[0004] Nevertheless, the dielectric ceramic small block antenna has an antenna pattern formed directly on a dielectric, providing a relatively narrow sensitive band width. Also, if the user carries the antenna in his hand and holds it close to his ear, the antenna is affected by the human body in terms of dielectric constant. The result is a disturbed electromagnetic field and an mismatched impedance, which in turn cause trouble in communications. To prevent such a problem from happening, the antenna is connected to a high frequency circuit via an impedance matching device.

[0005] Furthermore, since the high frequency circuit generally contains a distribution constant circuit that handles mismatching of the impedance, an unexpected electromagnetic coupling may possibly occur between a signal line and a closely placed GND line. The distribution constant circuit in some cases has an unexpected coupling capacity between wiring lines due to irregularity in dimensions of the wiring pattern and other reasons. These are causes for the disruption of the impedance from being matched in the high frequency circuit.

[0006] The irregularity in dimension precision of the wiring pattern (circuit pattern) needs to be solved by trimming and improvement in the precision of the print pattern of the distribution constant circuit. Therefore, strict control is needed on the length of lead wires and the amount of solder when electronics components are mounted. As briefly illustrated here, the distribution constant circuit for impedance matching has a problem that high precision is demanded in its manufacturing proc-

ess.

[0007] The high frequency circuit therefore very often includes an impedance matching device between circuit blocks as well as in the connection with the antenna, since the inclusion reduces reflected waves and better transmits signals.

[0008] A conventional impedance matching device installed in high frequency circuits is typically large in size because of a capacitor and a coil incorporated therein for adjustment.

[0009] Moreover, so as to impart the characteristics required by the circuit module to the impedance matching device, consideration needs to be paid to the applicable temperature range and the temperature characteristics of various components in the design of the circuit. Skilled engineers and their experiences were essential to such a design process. Should the design fail to produce desirable characteristics, complex adjustment such as trimming is inevitable.

[0010] Therefore, conventional impedance matching devices have a problem of complex design and manufacturing processes and accompanying high costs.

**SUMMARY OF THE INVENTION**

[0011] An object of the present invention is to offer an impedance matching device of a small size, low manufacturing cost and simple impedance matching capability.

[0012] In order to accomplish the object, the impedance matching device in accordance with the present invention includes:

35 a voltage-controlled variable capacitor, for receiving an input signal, including,

40 a non-linear dielectric thin film having a relative dielectric constant in accordance with a strength of an applied electric field,  
a first electrode formed on one of the sides of the non-linear dielectric thin film, and  
a second electrode formed on the opposite side of the non-linear dielectric thin film; and

45 a control voltage application section for applying a control voltage across the first and second electrodes.

[0013] The impedance matching device is, for example, mounted on a high frequency circuit substrate. The impedance matching device is arranged so that an input signal, such as a high frequency signal, is sent to the voltage-controlled variable capacitor.

[0014] The voltage-controlled variable capacitor is arranged so that the relative dielectric constant of the non-linear dielectric thin film varies as the DC control voltage is applied across the first and second electrodes, that the capacity of the voltage-controlled varia-

ble capacitor then varies according to the variation in the relative dielectric constant, and that the impedance of the path of the input signal is in accordance with that variation in the capacity.

[0015] Therefore, with the arrangement, the impedance can be matched to the desirable value, by simply controlling the control voltage according to the high frequency signal that is coupled to the voltage-controlled variable capacitor and flows through the first and second electrodes.

[0016] A conventional impedance matching circuit is arranged to include chip components, such as a coil, capacitor, and resistor, incorporated on a ceramic substrate. However, such an arrangement requires replacement of the chip components and physical adjustment to match impedances between circuit blocks.

[0017] By contrast, the impedance matching device in accordance with the present invention can always produce the desirable impedance simply by the control voltage application section changing the control voltage according to temperature variations and effects from the user. This facilitates design and adjustment of high frequency circuits.

[0018] In addition, the non-linear dielectric thin film is preferably made of an oxidized material such as BaTiO<sub>3</sub>, SrTiO<sub>3</sub>, or BaSrTiO<sub>3</sub>. The use of such a material can form the non-linear dielectric thin film on a ceramic substrate that produces little signal delay and that is suitable for a high frequency circuit substrate, by baking integrally with other passive parts such as a capacitor, coil, and resistor. This makes it possible to cut down the impedance matching device in size and cost.

[0019] For a fuller understanding of the nature and advantages of the invention, reference should be made to the ensuing detailed description taken in conjunction with the accompanying drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

[0020]

Fig. 1 is a perspective view showing an impedance matching device of the first embodiment in accordance with the present invention.

Fig. 2 is an explanatory view showing the cross-sectional view taken along line A-A of the impedance matching device shown in Fig. 1.

Fig. 3 is an explanatory diagram showing an equivalent circuit for the impedance matching device shown in Fig. 1.

Fig. 4 is a flow block diagram showing operations of fabricating the impedance matching device shown in Fig. 1 on a substrate.

Fig. 5 is a graph showing changes in the electric flux density of the non-linear dielectric thin film of the impedance matching device shown in Fig. 1 in response to changes in the electric field strengths applied thereacross.

Fig. 6 is a graph showing changes in the relative dielectric constant of the non-linear dielectric thin film of the impedance matching device shown in Fig. 1 in response to changes in the electric field strengths applied thereacross.

Fig. 7 is an explanatory drawing showing a Smith chart illustrating the process of impedance matching by the impedance matching device shown in Fig. 1.

Fig. 8 is an explanatory diagram showing an equivalent circuit for the impedance matching device shown in Fig. 1 to which other chip components are added.

Fig. 9 is a graph showing variations in the dielectric constant change rate of the non-linear dielectric thin film of the impedance matching device shown in Fig. 1 in response to the applied voltage due to differences in material and film forming conditions.

Fig. 10 is a graph showing changes in leak current of the non-linear dielectric thin film in response to the voltage applied thereacross when the thin film is made of a BST film.

Fig. 11 is a flow block diagram illustrating a manufacturing process of a non-linear dielectric thin film of an impedance matching device of the second embodiment in accordance with the present invention.

#### DESCRIPTION OF THE EMBODIMENTS

##### [First Embodiment]

[0021] The following description will discuss the first embodiment in accordance with the present invention.

[0022] Fig. 1 is a perspective view showing the structure of an impedance matching device 1 of an embodiment in accordance with the present invention. Fig. 2 is a cross-sectional view of the impedance matching device 1 taken along line A-A shown in Fig. 1. Fig. 3 is an explanatory diagram showing an equivalent circuit for the impedance matching device 1.

[0023] The impedance matching device 1 is connected to, for example, an impedance matched antenna or directional antenna or disposed between high frequency circuits. Figs. 1 to 3 do not show structure of peripheral circuits of the impedance matching device 1.

[0024] In the impedance matching device 1, high frequency signals are input to a variable capacitor (voltage-controlled variable capacitor) 4 via an input terminal 2 and a print resistor 3 and output from a control voltage application terminal 9 and an earth terminal 13.

[0025] The variable capacitor 4 includes a non-linear dielectric thin film 5 flanked by first and second electrodes 6 and 7. The lower electrode 6 is connected to a control voltage application terminal 8 and the print resistor 3. The upper electrode 7 is connected to the control voltage application terminal 9 and an input terminal of a

chip component 11 via a wiring pattern 10.

[0026] The control voltage application terminals 8 and 9 are connected to a control voltage application circuit (control voltage application section) 31, which generates a predetermined control voltage to apply across the control voltage application terminals 8 and 9. The chip component 11 is a load element having an inductor L and a resistor component R. The output terminal of the chip component 11 is connected to the earth terminal 13 via a wiring pattern 12.

[0027] The earth terminal 13 is connected via a through hole 15 formed through a substrate 14 to an earth pattern 16 formed on the opposite surface of the substrate 14. The substrate (high frequency circuit substrate) 14 is a multi-layer wiring substrate formed by baking, for example, glass materials and ceramic materials containing titanium oxide and barium oxide as main components. Inside the substrate 14, there are other high frequency circuits including a filter circuit.

[0028] An electric field of about 1MV/cm is arranged to be applied across the non-linear dielectric thin film 5. The power supply voltage is set to range, for example, from 5V to 10V, because of the demand for lower voltage with portable devices. For these reason, the non-linear dielectric thin film 5 needs to have a thickness of 0.1 $\mu$ m to 0.2 $\mu$ m.

[0029] The electrodes 6 and 7 are formed, taking the skin thickness thereof into consideration because the electrodes 6 and 7 handle high frequencies. So the electrodes 6 and 7 are 6 $\mu$ m to 7 $\mu$ m thick films. A thick film formed with the skin thickness thereof taken into consideration can be defined as follows: Typically, a high frequency signal tends to travel near the surface of a conductive body. The tendency becomes more evident as the frequency of the signal becomes higher. In other words, a thick film formed with the skin thickness thereof taken into consideration is a film having a proper thickness according to the frequency range of high frequency signals to be handled.

[0030] The impedance matching device 1 is, as explained above, constituted by both a thick film and a thin film. Therefore, the non-linear dielectric thin film 5 can be formed as a thin film by a deposition, printing, or sol-gel technique using an organic metal salt. Figs. 1 and 2 show an example of the non-linear dielectric thin film 5 formed by a MOD (Molecular Oxide Deposition) technique. The MOD technique is one of the printing techniques described above and capable of forming uniform non-linear dielectric thin films.

[0031] According to the MOD technique, MOD in a gel form is printed using a screen mask and then baked. The technique can coat a flat surface with a very uniform non-linear dielectric thin film, but fails to substantially cover up a step at an edge portion of the thick lower electrode 6 which acts as a bed, causing partial irregularity in the film thickness. This causes short-circuit of the electrodes 6 and 7, an increased leak current, and an abnormal insulation withstand voltage. In addi-

tion, the capacity may become irregular.

[0032] Therefore, as shown in Fig. 2, the impedance matching device 1 has an insulating film 17 formed on an edge portion of the lower electrode 6. The insulating film 17 is composed of a material having a substantially low relative dielectric constant, such as SiO<sub>2</sub> or Al<sub>2</sub>O<sub>3</sub>, compared to the non-linear dielectric thin film 5, and laid to form a thickness of not less than 2 $\mu$ m. The upper electrode 7 is disposed in the opening 17a in the insulating film 17 in Fig. 2. The insulating film 17 and the control voltage application circuit 31 are omitted from Fig. 1 for simplicity of the drawing.

[0033] The following description will explain a manufacturing process of the impedance matching device 1 arranged as above. Fig. 4 is a flow block diagram explaining the manufacturing process. As shown in Fig. 4, the manufacturing process of the impedance matching device 1 starts with Step s1, forming as a thick film a lower conductive part including the lower electrode 6 on the substrate 14 having the through hole 15 and the earth pattern 16. The lower conductive part is formed using one of, or a combination of, conductor paste, electroplating, and other techniques.

[0034] In Step s2a, the non-linear dielectric thin film 5 is formed by screen-printing and baking, for example, the MOD above. In Step s2b, the print resistor 3 is formed as shown in Fig. 1 by thin-film-printing.

[0035] The process proceeds to Step s3 after Step s2a and Step s2b are completed. In Step s3, the part of the non-linear dielectric thin film 5 that extends beyond the bounds of the lower electrode 6 is removed by a lift-off technique. A material that can be easily etched chemically, such as photoresist and Al, is used for the removal by the lift-off technique. The insulating film 17 is formed, also in Step s3; on an outer edge of an area that will act as the variable capacitor 4 by, for example, a spin coat or deposition technique.

[0036] In Step s4, an upper conductive part including the upper electrode 7 and the wiring pattern 10 is formed as a thick film. The upper conductive part is formed, similarly to the lower conductive part, using one of, or a combination of, printing, electroplating, and other techniques. Thereafter, in Step s5, a chip component 11 is mounted as shown in Figs. 1 and 2, which completes the manufacturing process of the impedance matching device 1.

[0037] The following description will explain impedance matching properties of the impedance matching device 1. Fig. 5 is a graph showing changes in the electric flux density D in the non-linear dielectric thin film 5 in response to changes of the voltage strengths applied thereto. As the control voltage application circuit 31 shown in Fig. 2 applies a DC control voltage across the electrodes 6 and 7 via the control voltage application terminals 8 and 9, an electric field is generated across the non-linear dielectric thin film 5, and the electric flux density D across the non-linear dielectric thin film 5 changes with the electric field strength E as shown in

Fig. 5.

[0038] Fig. 6 is a graph showing changes in the relative dielectric constant of the non-linear dielectric thin film 5 in response to changes in the voltage strengths applied thereacross. As the graph shows, the relative dielectric constant  $\epsilon_r$  is, for example, about 300 in the vicinity of  $E = 0\text{MV/cm}$  where the electric field strength  $E$  is weak, and 100 at  $E = \pm 1\text{MV/cm}$  where the electric field strength  $E$  is strong. It is therefore understood that the relative dielectric constant  $\epsilon_r$  of the non-linear dielectric thin film 5, i.e., the capacity of the variable capacitor 4, can change more than 60% by changing the applied electric field.

[0039] The following description will explain an example of the control of the capacity of the variable capacitor 4 required for impedance matching. As Fig. 3 shows, the impedance matching device 1 is arranged to match the impedance with the chip component 11, i.e. a load element, connected in series to a DC circuit that includes the print resistor 3 and the variable capacitor 4. Referring to the Smith chart in Fig. 7, the following description will explain in detail the capacity required with the variable capacitor 4 of the impedance matching device 1 of such an arrangement.

[0040] It is assumed in the description below that the characteristic impedance  $Z_c$  of the impedance matching device 1 is designed to be  $50\Omega$ . It is also assumed, as shown in Fig. 3, that the resistance of the print resistor 3 is  $25\Omega$ , the inductance of the inductor L of the chip component 11 is  $j25\Omega$ , and the resistance of the resistor component R is  $25\Omega$ , where  $j$  represents the imaginary unit.

[0041] In the description below, the reactance of the variable capacitor 4 will be referred to as  $X_c$ , and the real impedance of the impedance matching device 1 will be referred to as  $Z$ .

[0042] In such a case, the load impedance  $Z_1$  of the chip component 11 is:

$$Z_1 = 25 + j25 (\Omega) \quad (1)$$

[0043] The matching impedance  $Z_2$  composed of the print resistor 3 and the variable capacitor 4 is:

$$Z_2 = 25 - jX_c (\Omega) \quad (2)$$

Since  $Z_c = 50\Omega$ , dividing the impedances  $Z_1$  and  $Z_2$  above gives the normalised impedances  $Z'_1$  and  $Z'_2$  for the impedances  $Z_1$  and  $Z_2$ :

$$Z'_1 = 0.5 + j0.5 (\Omega) \quad (3)$$

$$Z'_2 = 0.5 - jX_c/0.5 (\Omega) \quad (4)$$

$Z'_1$  in Equation (3) corresponds to Point P1 in the Smith chart in Fig. 7.

[0044] The normalised impedance  $Z'$  of the impedance matching device 1 is equal to the sum of  $Z'_1$  and

$Z'_2$ . Therefore, so as to match the impedance of the impedance matching device 1 to  $50\Omega$ , the reactance  $X_c$  needs to be determined so that the normalised impedance  $Z'$  equals 1.0, i.e.:

$$Z'_1 + Z'_2 = 1.0 \quad (5)$$

In other words, the reactance  $X_c$  needs to satisfy:

$$0.5 - X_c/50 = 0 \quad (6)$$

From Equation (6),  $X_c = 25$  is obtained.

[0045] Here, supposing that the high frequency signals to be handled have a frequency of, for example, 1GHz, the capacity C of the variable capacitor 4 satisfies:

$$X_c = 1/(\omega C) = 1/(2\pi f C) \quad (7)$$

From Equation (7), it is understood that the desirable capacity C of the variable capacitor 4 for impedance matching is about  $6.37\text{pF}$ .

[0046] The impedance  $Z'$  is moved from Point P1 to Point P2 in Fig. 7 by the print resistor 3 of the insertion impedance  $Z_2$ , and then on to Point P3 by the variable capacitor 4. The impedance  $Z'$  consequently is  $Z' = 1.0$ , and the impedance  $Z$  is matched to  $Z_c = 50\Omega$ .

[0047] Also, as described above, the capacity C of the variable capacitor 4 is variable not less than 60%. Therefore, if the variable capacitor 4 is formed to have a capacity of about  $10\text{pF}$ , the impedance matching device 1 is capable of varying the capacity C of the variable capacitor 4 to about  $3.3\text{pF}$  by applying a control voltage across the variable capacitor 4.

[0048] As described above, the impedance matching device 1 is capable of matching the impedance to the predetermined characteristic impedance  $Z_c$  only by varying, according to the impedance of the chip component 11 that is a load element, the control voltage applied across the variable capacitor 4 by the control voltage application circuit 31.

[0049] The control voltage application circuit 31 is constituted, for example, by a voltage dividing resistor disposed between power supply lines and a trim resistor trimmed according to the actually measured load impedance  $Z_1$ , and generates a predetermined control voltage.

[0050] Fig. 8 is an explanatory diagram showing an equivalent circuit for the impedance matching device 1 incorporating a chip component 21 instead of the component chip 11. As Fig. 8 shows, the load impedance  $Z_1$  of the chip component 21 varies.

[0051] In that arrangement, the control voltage application circuit 31 detects a variation in the load impedance  $Z_1$  in the chip component 21 and changes the control voltage. The impedance matching device 1 can maintain the matching of impedance even when the load impedance  $Z_1$  of the chip component 21 that is a

load element varies.

[0052] The following description will explain characteristics of the non-linear dielectric thin film 5 and the electrodes 6 and 7 constituting the variable capacitor 4. Fig. 9 is a graph showing variations in the dielectric constant change rate of the non-linear dielectric thin film 5 in response to the applied voltage due to differences in material and film forming conditions for the non-linear dielectric thin film 5.

[0053] As clearly understood from the graph, the dielectric constant change rate of the non-linear dielectric thin film 5 varies greatly depending upon film forming conditions such as film forming methods and baking conditions. Note that the dielectric constant change rate here refers to the value of  $(\Delta\epsilon/\epsilon)$  where  $\epsilon$  is the dielectric constant and  $\Delta\epsilon$  is the amount of change of the dielectric constant.

[0054] The graph also shows that effectual materials for the non-linear dielectric thin film 5 include STO ( $\text{SrTiO}_3$ ) and BST ( $\text{Ba}_x\text{Sr}_{1-x}\text{TiO}_3$  ( $0 < x < 1$ , e.g.  $x = 0.7$ ). Especially effectual is BST used as the material in combination with a sol-gel technique as the film forming method. Besides STO and BST, BTO ( $\text{BaTiO}_3$ ) (not shown in the graph) also is an effectual material and used widely.

[0055] Fig. 10 is a graph showing results of measuring the variable capacitor 4 for the characteristics of leak current versus applied voltage. The non-linear dielectric thin film 5 of the variable capacitor 4 used for the measurement is made of a BST film as a material and baked at a temperature of  $700^\circ\text{C}$  by a sol-gel technique. The electrodes 6 and 7 are made of Pt/Ta (a laminated layer of Pt and Ta).

[0056] As Fig. 10 shows, the leak current of the variable capacitor 4 is extremely low, and it is understood that the material and film forming method mentioned above are effectual.

[0057] Also, as described above, the capacity C of the variable capacitor 4 can be obtained from Equations (1) to (7). The capacity C satisfies Equation:

$$C = \epsilon_r \cdot S/t \quad (8)$$

where S is the area of the upper electrode constituting the capacitor, or in other words, the area of the opening 17a where the insulating film 17 is not formed over the non-linear dielectric thin film 5, t is the thickness of the non-linear dielectric thin film 5, and  $\epsilon_r$  is the relative dielectric constant of the non-linear dielectric thin film 5.

[0058] If above mentioned BST or STO is used for a material of the non-linear dielectric thin film 5, the relative dielectric constant  $\epsilon_r$  is about 300. The value is extremely large compared to a case where such a material as  $\text{SiO}_2$  is used. Therefore, since the use of such materials as mentioned above results in a smaller  $S/t$  still producing the same capacity C, the non-linear dielectric thin film 5 can be made smaller in the area thereof.

[0059] Also, as described above, an electric field as strong as  $\pm 1\text{MV}/\text{cm}$  is applied across the non-linear dielectric thin film 5 so as to change the relative dielectric constant  $\epsilon_r$ . The thinner the thickness t of the non-linear dielectric thin film 5, the stronger the applied electric field produced by applying a low voltage is. This makes it possible to reduce the thickness t and thereby manufacture a thin type of non-linear dielectric thin film 5, i.e. a much smaller type of non-linear dielectric thin film 5.

[0060] As described above, the relative dielectric constant  $\epsilon_r$  of the non-linear dielectric thin film 5 is variable not less than 60%. Therefore, 30% thereof may be used for impedance matching, i.e., as a variable range of the impedance Z2, while the remaining 30% may be used for compensating for the irregularity in dimension precision of the capacitor that is likely to be caused by the reduction of the non-linear dielectric thin film 5 in size. This eases requirements on design and manufacture precision of the non-linear dielectric thin film 5.

[0061] In other words, the variable capacitor 4 may be arranged to compensate, using part of the change in the capacity C caused by a change in the relative dielectric constant of the non-linear dielectric thin film 5, for an irregularity in the capacity C caused by irregularity in design and manufacture. The irregularity in the capacity refers to the difference between the actual capacity and the capacity predetermined in design of the variable capacitor 4 of the impedance matching device 1.

[0062] Also, as described above, the thinner the thickness t of the non-linear dielectric thin film 5, the stronger the applied electric field produced by applying a low voltage is. However, in the light of crystallinity, convexities, concavities, and pin holes of the bed substrate, it is believed that the thickness t has a lower limit of about  $0.05\mu\text{m}$ . Assuming that the practical upper limit for the applied voltage is 100V, the upper limit of the thickness t is  $10\mu\text{m}$ .

[0063] The impedance matching device 1 can be thus realised equipped with the variable capacitor 4 having a capacity variable according to the applied DC control voltage. Then by changing the DC control voltage applied across the variable capacitor 4, a capacity can be realised easily that has the reactance in accordance with the impedance to be matched.

[0064] This enables the impedance matching device 1 to be capable of matching impedance with a smaller and cheaper arrangement. In addition, desired characteristics as shown in Figs. 5 and 6 can be realised with high precision by merely applying a control voltage in accordance with the characteristics of the variable capacitor 4, easing requirements on design and manufacture precision. Moreover, the impedance matching device 1 has an extremely small temperature coefficient and still maintains stable characteristics, compared to variable capacitors using semiconductor.

[0065] Furthermore, as to the impedance matching device 1, after printing and thus forming the non-linear dielectric thin film 5 on the lower electrode 6 that acts as

a bed conductor, the portion of the printed non-linear dielectric thin film 5 extending over the lower electrode 6 is removed by a photo etching technique, or preferably a lift-off technique, only leaving the portion that will fabricated into the variable capacitor 4.

[0066] Therefore, the impedance matching device 1 includes an arrangement of the non-linear dielectric thin film 5 formed on a flat conductor surface, thereby being capable of restraining the leak current and realising a satisfactory insulation withstand voltage. That arrangement also restrains possible irregularity in the capacity of the variable capacitor 4, imparting desirable characteristics.

[0067] Also, as to the impedance matching device 1, the electrode 7 that acts as the upper electrode is formed after the outer circumferential edge of the non-linear dielectric thin film 5 is covered with the insulating film 17 having a small dielectric constant. Therefore, the impedance matching device 1 restrains the leak current and increases the insulation withstand voltage.

[0068] Moreover, even when the relative dielectric constant  $\epsilon_r$  of the non-linear dielectric thin film 5 is large, the impedance matching device 1 still can reduce the capacity of the variable capacitor 4 by forming the opening 17a of the insulating film 17 with an extremely small area of, for example,  $0.1\text{mm} \times 0.1\text{mm}$ . Consequently, a desirable small capacity suitable for high frequency can be obtained with good reproducibility.

#### [Second Embodiment]

[0069] The following description will discuss the second embodiment in accordance with the present invention. Here, for convenience, members of the present embodiment that have the same arrangement and function as members of the previous embodiment, and that are mentioned in the previous embodiment are indicated by the same reference numerals and description thereof is omitted.

[0070] In the present embodiment will be explained a manufacturing process of the non-linear dielectric thin film 5 detailed in the first embodiment by a print lamination technique. Fig. 11 is a flow block diagram illustrating the manufacturing process. As Fig. 11 shows, in Step s11, a raw material is prepared by baking using a temporary baker and grinding using a grinder. In Step s12, the raw material is made into slurry by a ball mill and a stirring vacuum dearrator. In Step s13, the raw material made into slurry is shaped into a green sheet. In Step s14, conductors including the electrodes 6 and 7 are formed on the green sheet formed in Step s13 by printing. In Step s15, the green sheet is laminated by a sheet press machine. In Step s16, the laminated green sheet is baked by a baker, which completes the manufacturing process of the non-linear dielectric thin film 5.

[0071] The formation of the non-linear dielectric thin film 5 by a conventional green sheet process in the above manner can reduce the number of steps that

need to be newly developed, and makes it possible to cut down the impedance matching device 1 in size, since the non-linear dielectric thin film 5 can be formed integrally with the substrate 14. Incidentally, the non-linear dielectric thin film 5 may be constituted by either a single or a plurality of green sheets: for example, if the non-linear dielectric thin film 5 needs to be formed relatively thick, two or more green sheets may be laminated on the substrate 14.

5 [0072] As discussed so far, a first impedance matching device in accordance with the present invention is an impedance matching device mounted on a high frequency circuit substrate, and is arranged to include:

10 a voltage-controlled variable capacitor including,

20 a non-linear dielectric thin film having a relative dielectric constant that varies in accordance with a strength of an applied electric field,  
a first electrode formed on one of the sides of the non-linear dielectric thin film, and  
a second electrode formed on the opposite side of the non-linear dielectric thin film; and

25 a control voltage application section for applying a control voltage across the first and second electrodes,

wherein the voltage-controlled variable capacitor is for receiving the control voltage and a high frequency signal for generating an electric field across the two electrodes.

[0073] With the arrangement, as a DC control voltage is applied across the electrodes, the relative dielectric constant of the non-linear dielectric thin film varies. Therefore, the capacity and, hence, the impedance of the voltage-controlled variable capacitor including the non-linear dielectric thin film can be set to a desirable value in accordance with the high frequency signal flowing through the two electrodes.

[0074] In other words, the first impedance matching device above includes on a high frequency circuit substrate a voltage-controlled variable capacitor constituted by a non-linear dielectric thin film that is flanked by two electrodes and that has a relative dielectric constant variable in accordance with a strength of an applied electric field. The relative dielectric constant of the non-linear dielectric thin film is changed by the control voltage application means applying a DC control voltage across the two electrodes. Therefore, the capacity and, hence, the impedance of the voltage-controlled variable capacitor can be set to a desirable value in accordance with the high frequency signal flowing through the two electrodes.

[0075] The conventional MCM (Multi-Chip Module) technology of forming a desirabl circuit by mounting chip components on a ceramic substrate requires replacement of such chip components, as an L, C, and

R, and physical adjustment to match impedances between circuit blocks.

[0076] By contrast, the first impedance matching device above can always produce a desirable impedance simply by the control voltage application section changing the control voltage according to temperature variations and effects from the user. This facilitates design and adjustment of high frequency circuits.

[0077] In addition, if the non-linear dielectric thin film is made of an oxidized material such as BaTiO<sub>3</sub>, SrTiO<sub>3</sub>, or BaSrTiO<sub>3</sub>, a non-linear dielectric thin film can be formed on a ceramic substrate that produces little signal delay and that is suitable to act as a high frequency circuit substrate, by baking integrally with other passive parts like a capacitor, a coil, and a resistor. This makes it possible to cut down the impedance matching device in size and cost.

[0078] A second impedance matching device in accordance with the present invention, incorporating the arrangement of the first impedance matching device above, is further arranged so that the non-linear dielectric thin film is made of BaTiO<sub>3</sub>, SrTiO<sub>3</sub>, or Ba<sub>x</sub>Sr<sub>1-x</sub>TiO<sub>3</sub> ( $0 < x < 1$ ).

[0079] With the arrangement, those materials can produce a variation of a greater extent in the relative dielectric constant in response to a predetermined applied voltage than other non-linear dielectric materials. Ba<sub>x</sub>Sr<sub>1-x</sub>TiO<sub>3</sub> is especially suitable because it produces a variation of a great extent in the relative dielectric constant and a small leak current. Also with the arrangement, the non-linear dielectric thin film is made of an oxidized film. Therefore, the non-linear dielectric thin film can be formed integrally with other passive parts like a capacitor, a coil, and a resistor by a ceramic lamination process for a high frequency circuit substrate. Moreover, the conventional process of forming a ceramic substrate can be adopted through modification thereof by the mere addition of the step of forming the non-linear dielectric thin film, reducing the number of steps that need to be newly developed.

[0080] Furthermore, a third impedance matching device in accordance with the present invention, incorporating the arrangement of the second impedance matching device above, is further arranged so that the non-linear dielectric thin film has a thickness of 0.05μm to 10μm.

[0081] An exceedingly thick non-linear dielectric thin film would result in too large an applied voltage required to produce a variation of a desired extent in the relative dielectric constant. Meanwhile, an exceedingly thin non-linear dielectric thin film would result in an larger leak current due to the creation of pin holes by convexities and concavities in the bed and defects on the surface. By contrast, if the non-linear dielectric thin film has a thickness in the above-mentioned range, the variation of a desirable extent in the relative dielectric constant can be realised with a low applied voltage and a small leak current.

[0082] A fourth impedance matching device in accordance with the present invention, incorporating any of the arrangements of the first to third impedance matching devices above, is further arranged so that the non-linear dielectric thin film is formed directly on a high frequency circuit substrate made of a dielectric ceramic substrate or a glass substrate by a deposition, printing, or sol-gel technique using an organic metal salt.

[0083] With the arrangement, the desirable extremely thin non-linear dielectric thin film can be formed by a simple process.

[0084] A fifth impedance matching device in accordance with the present invention, incorporating the arrangement of the fourth impedance matching device above, is further arranged so that the non-linear dielectric thin film is formed by a photo etching technique, or preferably a lift-off technique.

[0085] With the arrangement, the outer circumferential edge of the non-linear dielectric thin film is removed that extends over the bed electrode formed as a thick film and that does not affect the variable capacity.

[0086] Consequently, only the portion, of the printed non-linear dielectric thin film, formed uniformly on the bed electrode is used for the variable capacity. This makes it possible to restrain leak current and realise a substantial insulation withstand voltage. This also makes it possible to restrain irregularity in capacity, producing desirable properties.

[0087] A sixth impedance matching device in accordance with the present invention, incorporating the arrangement of the fourth or fifth impedance matching device above, is further arranged so that the non-linear dielectric thin film has an outer circumferential edge covered with an insulating film made of a material having a low relative dielectric constant.

[0088] With the arrangement, the upper electrode is formed after the insulating film is formed to cover the outer circumferential edge of the non-linear dielectric thin film, where the non-linear dielectric thin film is likely to be non-uniform. This makes it possible to restrain leak current and realise a substantial insulation withstand voltage. This also makes it possible to restrain irregularity in capacity, producing desirable properties.

[0089] A seventh impedance matching device in accordance with the present invention, incorporating any of the arrangements of the first to third impedance matching devices above, is further arranged so that the non-linear dielectric thin film is formed on the high frequency circuit substrate by a ceramic green sheet process.

[0090] With the arrangement, the non-linear dielectric thin film can be formed integrally on the high frequency circuit substrate by a conventional green sheet process. This can reduce the number of steps that need to be newly developed.

[0091] An eighth impedance matching device in accordance with the present invention, incorporating any of the arrangements of the first to seventh impedi-

ance matching devices above, is further arranged so that the electrodes are thick film conductive bodies formed with the skin thicknesses thereof taken into consideration.

[0092] With the arrangement, the desirable electric properties including capacity can be surely imparted to the voltage-controlled variable capacitor even when the high frequency signals to be handled have high frequencies.

[0093] The invention being thus described, it will be obvious that the same may be varied in many ways. Such variations are not to be regarded as a departure from the spirit and scope of the invention, and all such modifications as would be obvious to one skilled in the art intended to be included within the scope of the following claims.

#### Claims

1. An impedance matching device (1) mounted on a high frequency circuit substrate, being characterised in that said device comprises:

a voltage-controlled variable capacitor (4), for receiving an input signal, including,

a non-linear dielectric thin film (5) having a relative dielectric constant in accordance with a strength of an applied electric field, a first electrode (6) formed on one of the sides of the non-linear dielectric thin film (5), and  
a second electrode (7) formed on the opposite side of the non-linear dielectric thin film (5); and

a control voltage application section (31) for applying a control voltage across the first and second electrodes (6, 7).

2. The impedance matching device (1) as set forth in claim 1,

wherein the non-linear dielectric thin film (5) is made of  $\text{BaTiO}_3$ ,  $\text{SrTiO}_3$ , or  $\text{Ba}_x\text{Sr}_{1-x}\text{TiO}_3$  ( $0 < x < 1$ ).

3. The impedance matching device (1) as set forth in claim 2,

wherein the non-linear dielectric thin film (5) has a uniform thickness in a range of  $0.05\mu\text{m}$  to  $10\mu\text{m}$ .

4. The impedance matching device (1) as set forth in any one of claims 1 to 3,

wherein the non-linear dielectric thin film (5) is formed directly on a high frequency circuit substrate (14) made of a dielectric ceramic substrate or a glass substrate.

5. The impedance matching device (1) as set forth in claim 4,  
wherein the non-linear dielectric thin film (5) is formed directly on the high frequency circuit substrate (14) by a deposition technique, a printing technique, or a sol-gel technique using an organic metal salt.

6. The impedance matching device (1) as set forth in claim 5,  
wherein the non-linear dielectric thin film (5) is formed (14) by a photo etching technique.

7. The impedance matching device (1) as set forth in claim 6,  
wherein the photo etching technique is a lift-off technique.

8. The impedance matching device (1) as set forth in claim 1,  
wherein the non-linear dielectric thin film (5) is made of  $\text{Ba}_x\text{Sr}_{1-x}\text{TiO}_3$  ( $0 < x < 1$ ) by a sol-gel technique.

25 9. The impedance matching device (1) as set forth in any one of claims 4 to 6,  
wherein the non-linear dielectric thin film (5) has an outer circumferential edge covered with an insulating film (17) made of a material having a low relative dielectric constant.

30 10. The impedance matching device (1) as set forth in claim 9,  
wherein the insulating film (17) is made of  $\text{SiO}_2$  or  $\text{Al}_2\text{O}_3$ .

35 11. The impedance matching device (1) as set forth in any one of claims 1 to 3,  
wherein the non-linear dielectric thin film (5) is formed on a high frequency circuit substrate (14) by a ceramic green sheet process.

40 12. The impedance matching device (1) as set forth in any one of claims 1 to 11,  
wherein the first and second electrodes (6, 7) each are a thick film conductive body formed with a skin thickness thereof taken into consideration.

45 13. The impedance matching device (1) as set forth in claim 12,  
wherein the first and second electrodes (6, 7) each are made of a laminated layer of Pt and Ta.

50 14. The impedance matching device (1) as set forth in claim 1,  
wherein the voltage-controlled variable capacitor (4) compensates, using part of a change in a capacity caused by a change in a relative dielectric

constant of the non-linear dielectric thin film (5), for  
an irregularity in design and manufacture.

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FIG. 1

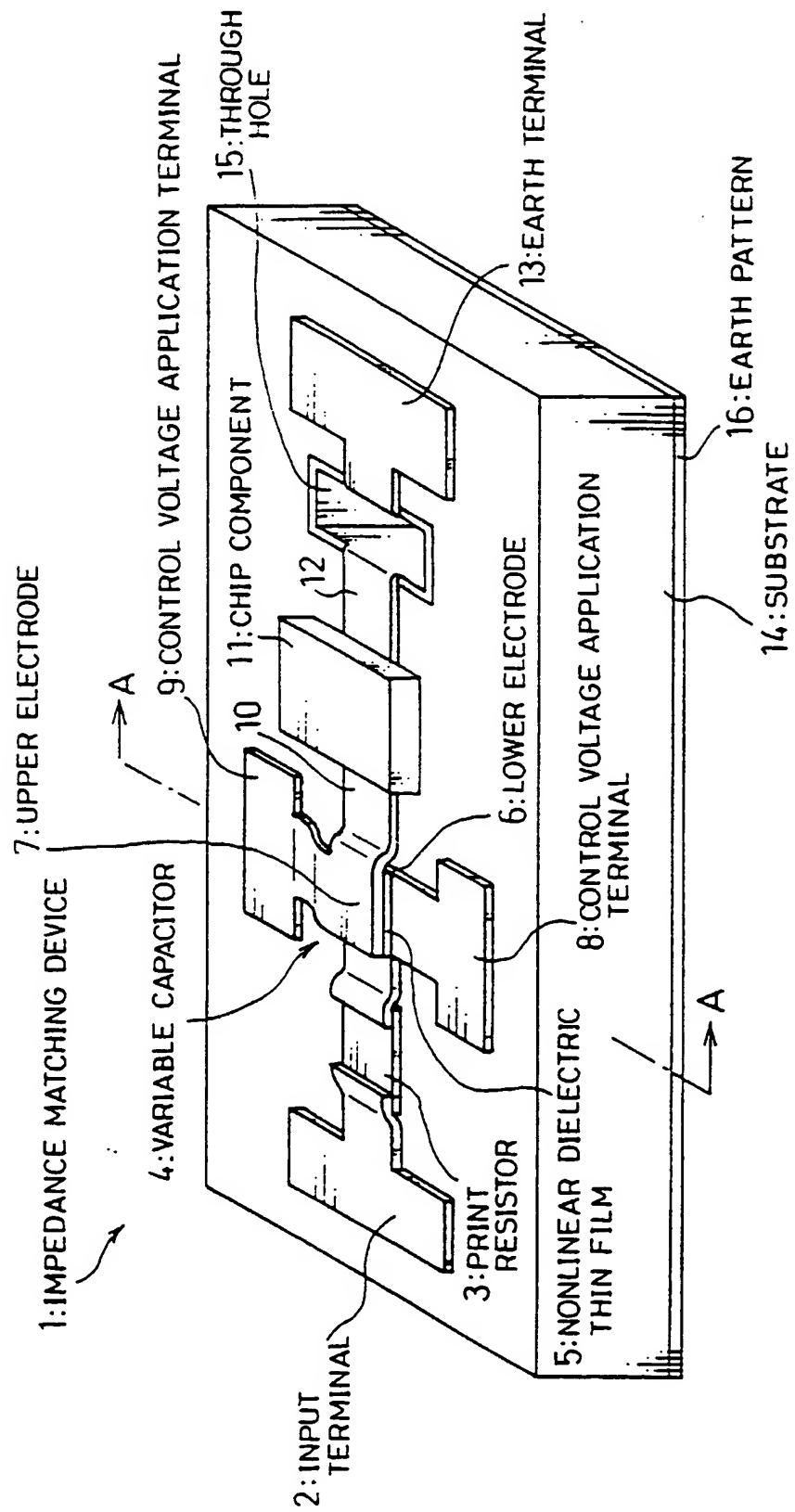


FIG.2

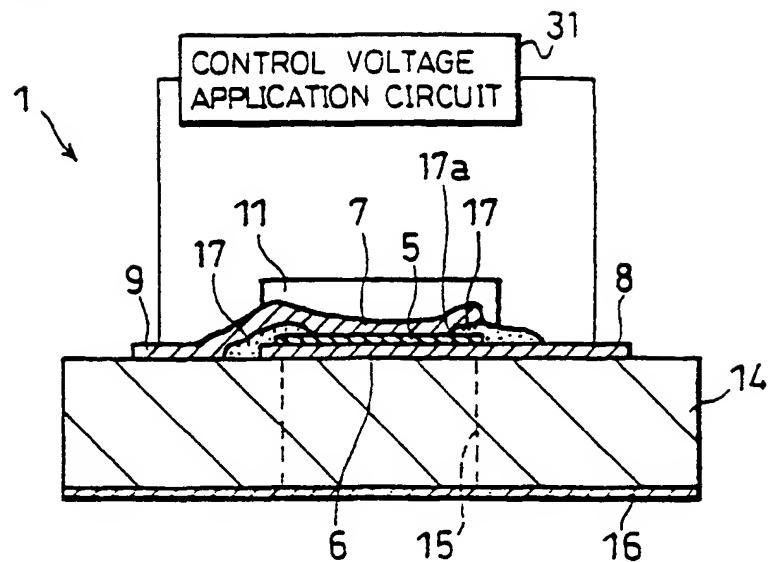
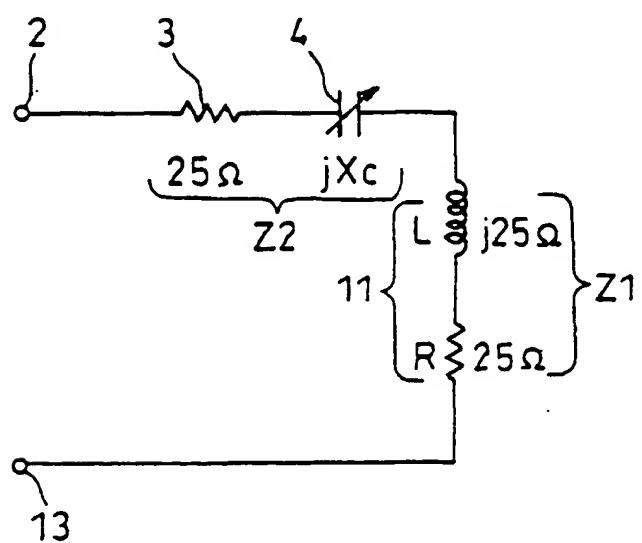


FIG.3



F I G . 4

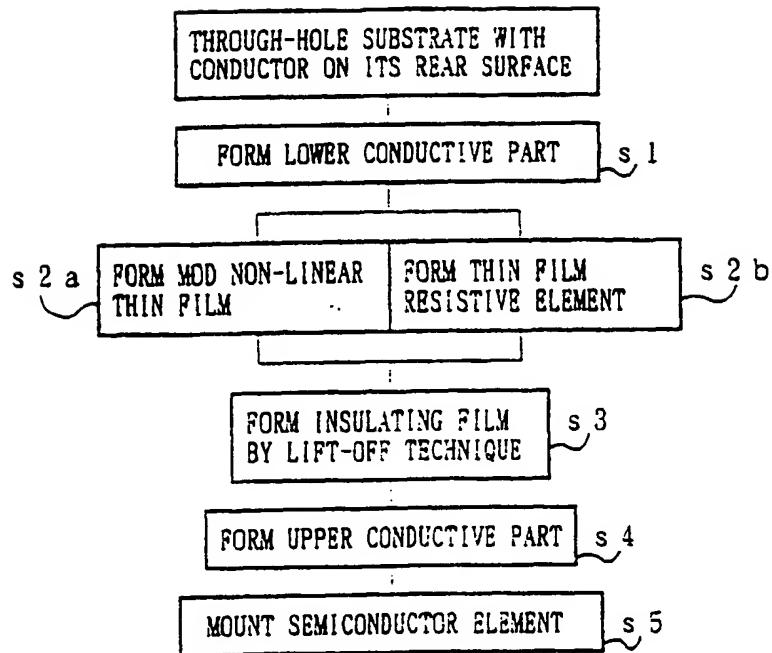


FIG.5

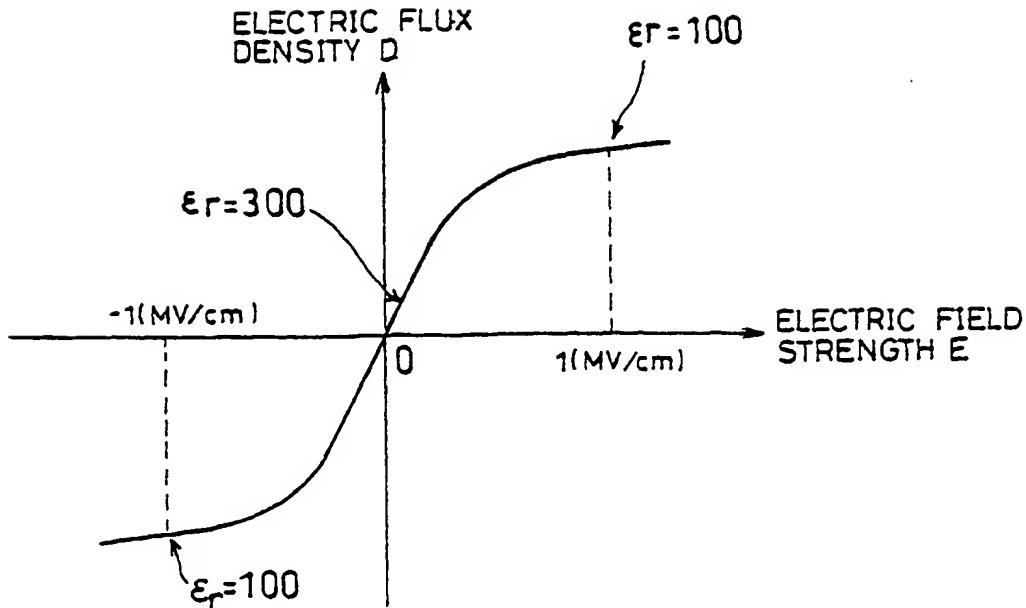
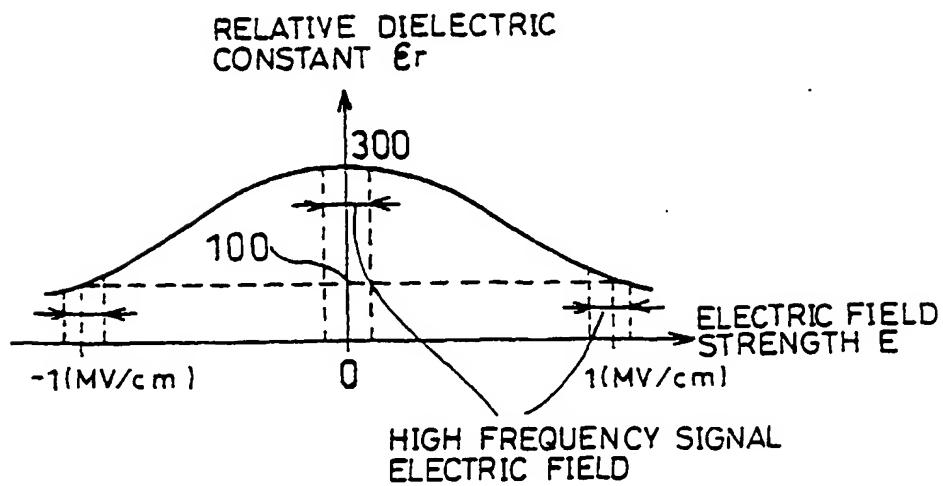


FIG.6



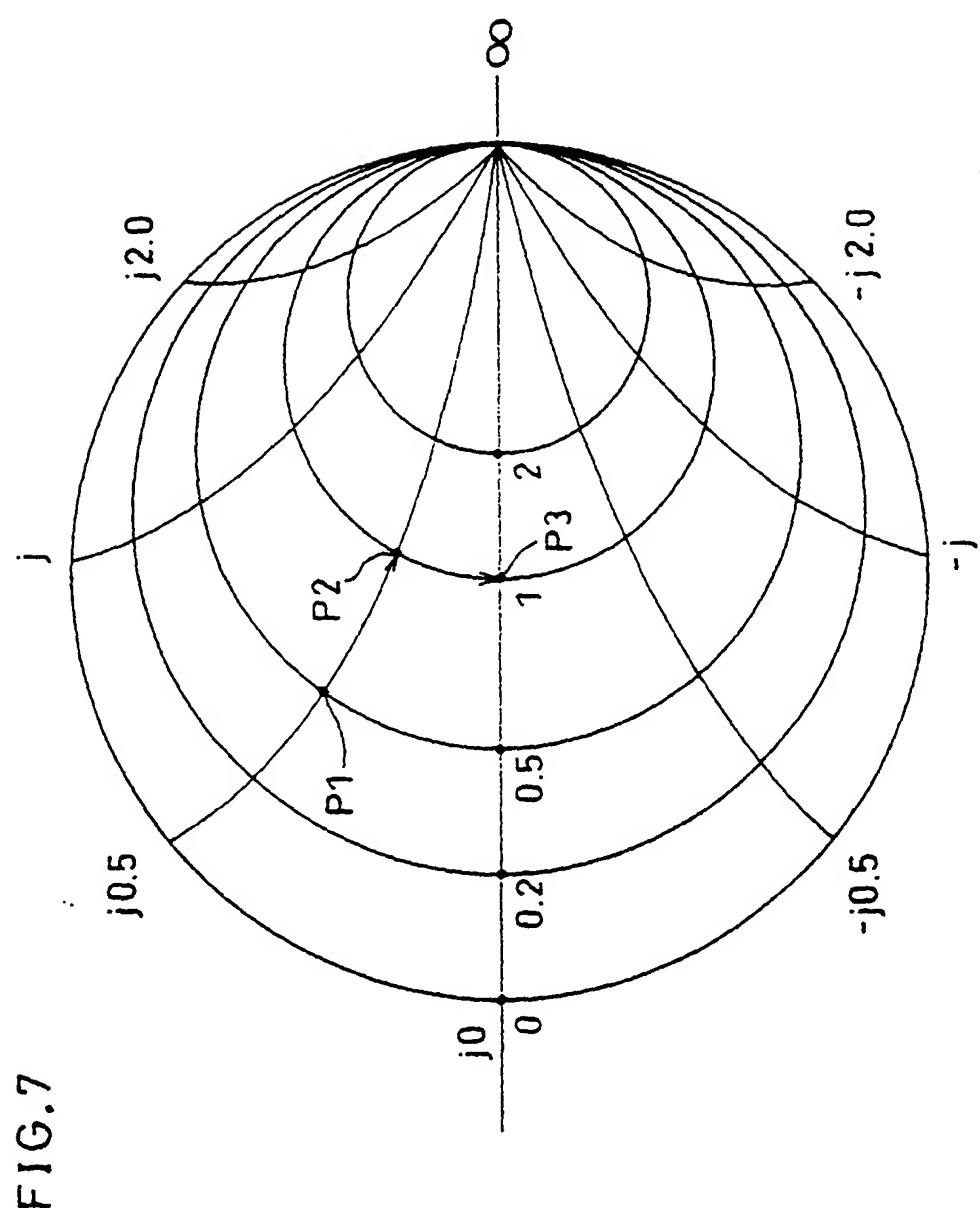


FIG. 7

FIG.8

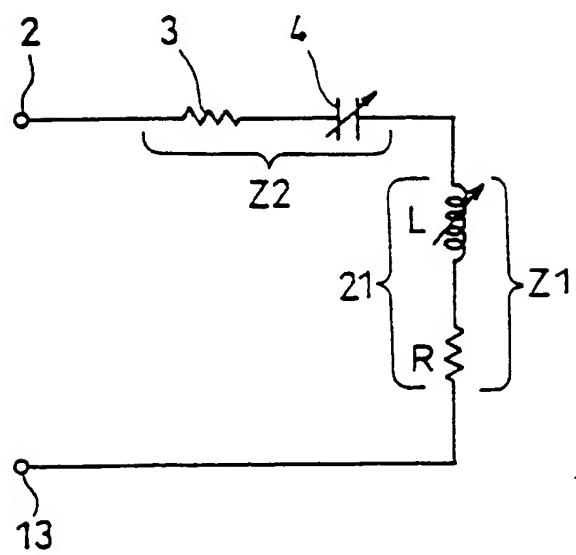
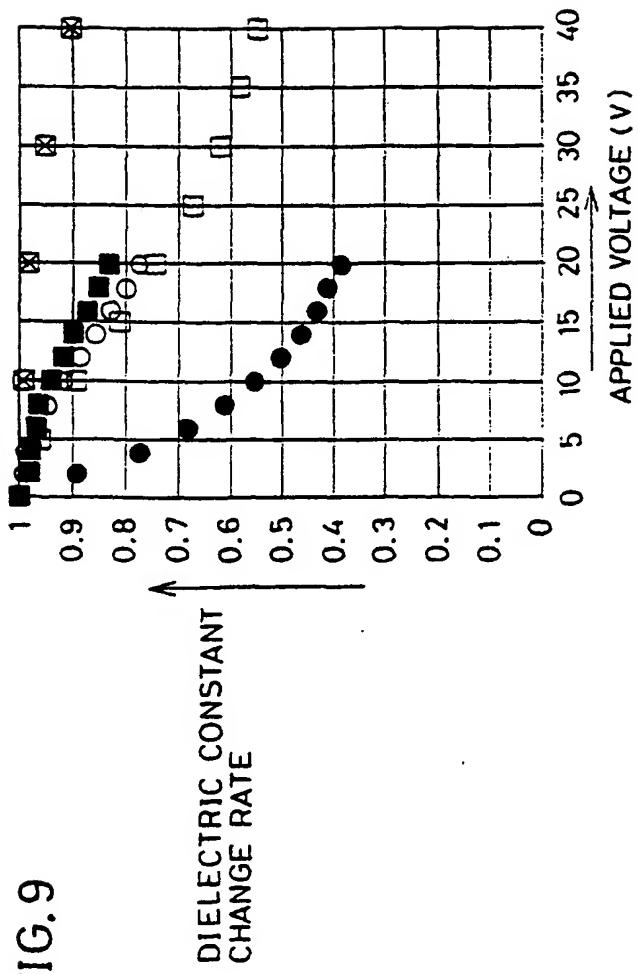
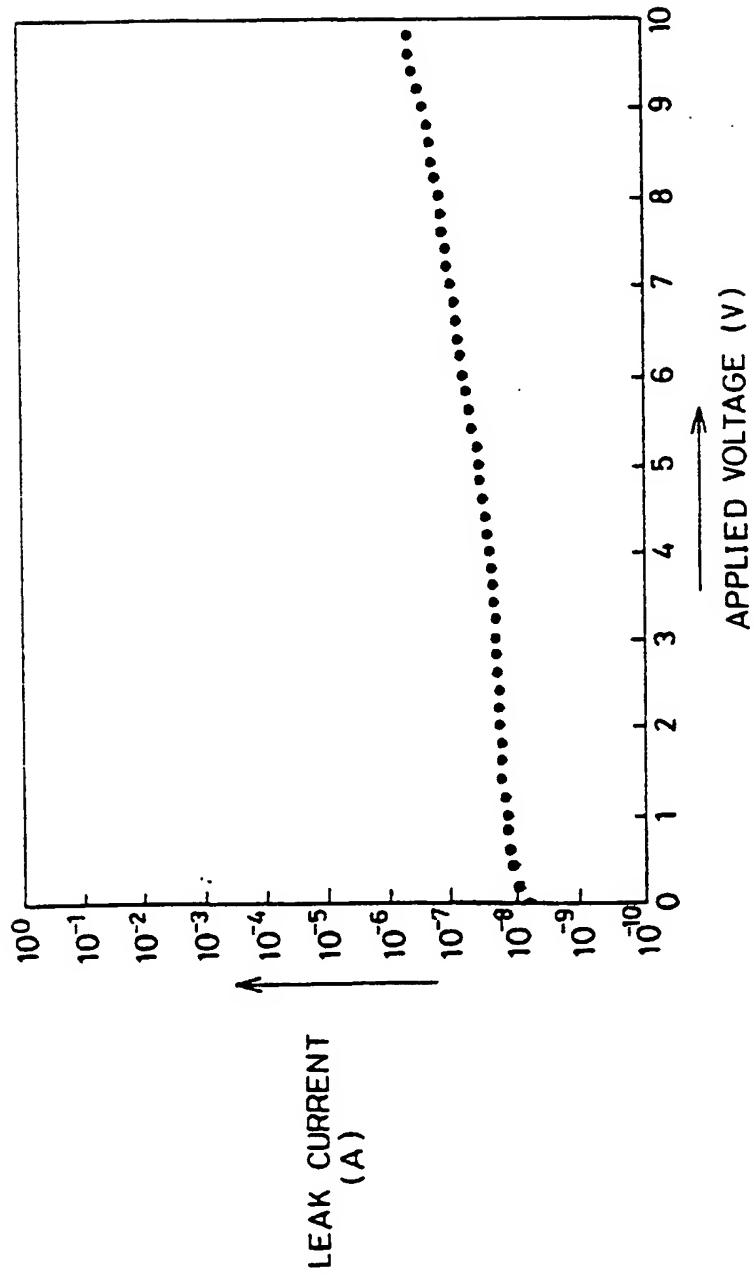


FIG. 9



- SOL-GEL BST FILM (ZBT09, TEMPORARY BAKING 400°C, REGULAR BAKING 600°C, t = 227 nm, CAP50 μm × 28 μm)
- SPUTTER BST FILM (SBT04, Th = 600°C, t = 230 nm, CAP53 μm × 26 μm)
- SOL-GEL STO FILM (ZST04, TEMPORARY BAKING 400°C, REGULAR BAKING 600°C, t = 220 nm, CAP50 μm × 13 μm)
- SPUTTER STO FILM (t = 240 nm, Ts = 350°C, Ar / O<sub>2</sub> = 3 / 7, P = 2 Pa, 0 to 1 kPa, 5 MINUTES COOLING)
- ☒ SPUTTER STO FILM (t = 393 nm, Ts = 340°C, Ar / O<sub>2</sub> = 0 / 10, P = 2 Pa, 0 to 1 kPa, 5 MINUTES COOLING)

FIG.10



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